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BSI Standards Publication

Mechanical standardization of semiconductor devices

Part 6-19: Measurement methods of the package warpage at elevated temperature and the maximum permissible warpage

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The UK participation in its preparation was entrusted to Technical Committee EPL/47, Semiconductors.

A list of organizations represented on this committee can be obtained on request to its secretary.

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Normalisation mécanique des dispositifs
à semiconducteurs -
Partie 6-19: Méthodes de mesure
du gauchissement des boîtiers
à température élevée
et du gauchissement maximum admissible
(CEI 60191-6-19:2010)

Mechanische Normung von Halbleiterbauelementen – Teil 6-19: Messverfahren für die Gehäuse-Verbiegung bei erhöhter Temperatur und die maximal zulässige Verbiegung (IEC 60191-6-19:2010)

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